

**Notice of References Cited**

Application/Control No.

09/259,145

Applicant(s)/Patent Under  
Reexamination  
PAN ET AL.

Examiner

Anh D. Mai

Art Unit

2814

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP-61-159741	07-1986	Japan	Noda et al.	H01L 21/322
	O	JP-05-109736	04-1993	Japan	Murakami	H01L 21/322
	P					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf et al., Silicon Processing for ther VLSI Era, Vol. 1, Lattice Press, 1986, pp. 262-265. <i>&amp; Full TEXT</i>
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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